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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/709,609	05/18/2004	William Wang	12790-US-PA	3608
	7590 07/28/200 N INTELLECTUAL P	EXAMINER		
7 FLOOR-1, N	IO. 100	VIDWAN, JASJIT S		
TAIPEI, 100	ROAD, SECTION 2	ART UNIT	PAPER NUMBER	
TAIWAN		2182		
			NOTIFICATION DATE	DELIVERY MODE
			07/28/2008	ELECTRONIC

# Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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USA@JCIPGROUP.COM.TW Belinda@JCIPGROUP.COM.TW

		Application	ı No.	Applicant(s)		
Office Action Summary		10/709,609	WANG, WILLIAM			
		Examiner		Art Unit		
		JASJIT S. V	/IDWAN	2182		
The MAILING DATE Period for Reply	of this communication a	appears on the	cover sheet with the d	correspondence ad	ldress	
A SHORTENED STATUTO WHICHEVER IS LONGER  - Extensions of time may be available after SIX (6) MONTHS from the ma  - If NO period for reply is specified at  - Failure to reply within the set or extended and the set of extended and the set of th	FROM THE MAILING under the provisions of 37 CFR ing date of this communication. ove, the maximum statutory perinded period for reply will, by stal r than three months after the ma	DATE OF THI 1.136(a). In no even od will apply and will tute, cause the applic	S COMMUNICATION t, however, may a reply be tire expire SIX (6) MONTHS from ation to become ABANDONE	N. nely filed the mailing date of this c D (35 U.S.C. § 133).		
Status						
2a)⊠ This action is <b>FINAL</b> 3)□ Since this application	unication(s) filed on <u>01</u> 2b)∏ Tl is in condition for allov with the practice unde	his action is no wance except fo	or formal matters, pro		e merits is	
Disposition of Claims						
4)	n(s) is/are withd allowed. g is/are rejected. objected to.	Irawn from cons				
	signated to but be Evens	inar				
	n is/are: a) ☐ a est that any objection to the heet(s) including the corn	accepted or b) he drawing(s) be ection is required	held in abeyance. Set if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CI		
Priority under 35 U.S.C. § 119	)					
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
Attachment(s)  1) Notice of References Cited (PTC2) Notice of Draftsperson's Patent 3) Information Disclosure Statemer Paper No(s)/Mail Date	Drawing Review (PTO-948)		1) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate		

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#### **DETAILED ACTION**

### Response to Arguments

1. Applicant's arguments/amendments see remarks, filed 04/01/08, with respect to the rejection(s) of claim(s) 1-5 & 7-10 under Pattisam have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Geiger et al.

## Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-5, 7-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Geiger et al, U.S. Pub No: 2003/0061457 [herein after Geiger] and further in view of Karpoff et at U.S. Patent No: 6,857,059 [herein after Karpoff].
- 1. **As per Claim 1 & 5**, Geiger teaches a data compression/decompression device, suitable for compressing/decompressing, an uncompressed/compressed data transmitted between a data generation device and a data storage device, comprising:
  - (a) Input buffer [see Fig. 11, element 252], for buffering and storing said uncompressed/compressed data for input [see Paragraph 0135 "...As shown, the DME channel 204 may write data (compressed or uncompressed) to the input buffer..."]
  - (b) Output buffer [see Fig. 11, element 254], for buffering and storing said data for output [see Paragraph 0135 "...and read resultant data produced by the codec 250 (uncompressed or compressed, respectively) from the output buffer]

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(c) Data compressor/decompressor [see Fig. 11, element 250], coupled to said output buffer, for compressing/decompressing said data for input and storing said data for output in said output buffer [see Paragraph 0140]

(d) Controller [see Fig. 12, element 204], coupled to said input buffer, said output buffer and said data compressor/decompressor, for controlling data transmission with said data generation device and said data storage device controlling compressing/decompressing said data [see Paragraphs 0142 - 0145]

Geiger teaches the above limitations in addition to teaching a system wherein the controller provides address mapping in relation to data provided by the data generation device and the storage disk [see Paragraphs 0146-0155]. However despite the above teachings, Pattisam fails to expressly teach performing the above function by way of "address mapping table" which is the cross reference between an access address transmitted from data generation device and physical address of storing the data in said data storage device.

Karpoff teaches the above deficiency by teaching a system wherein the microprocessor manages the mapping table which is the cross reference between an access address transmitted from data generation device (host) and a physical address of storing the data in data storage device [see Karpoff, Col. 4, Lines 23-36]. It would have been obvious to one of ordinary skill in the art at the time of Applicant's invention to combine the two teachings in order to take advantage of having a system where the host application never has to deal with volume resizing and spare capacity can be amortized across multiple disk images, thus lowering the cost associated with "on reserve" storage capacity [see Karpoff, Col. 3, Lines 46-64]

- 2. **As per Claim 2 and 7**, Geiger as modified by Karpoff above teaches a device wherein said controller includes:
  - (a) Data generation control unit, for controlling data transmission with said data generation device [see Fig. 10, element 500 operating on element 110]
  - (b) Data storage control unit, for controlling data transmission with said data storage device [see Fig. 10, element 310]

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(c) Data extractor, for obtaining said data from said input buffer, extracting a compressing/decompressing portion of said data, and sending said compression/decompression portion of said data to said data compressor / decompressor [see Paragraph 0160]

- (d) Main control unit for coordinating and controlling said data generation control unit, said data storage control unit, and said data extractor, and for managing said address mapping [see Fig. 5B, element 100]
- 3. **As per Claim 3 and 8**, Geiger as modified by Karpoff above teaches a device wherein said data generation device is at least one of a host, a laptop computer, a microprocessor, an interface card and a router [see Fig. 8, element 100]
- 4. **As per Claim 4 and 9,** Geiger as modified by Karpoff above teaches a device wherein said data storage device is at least one of a hard disk drive, floppy disk drive, a CD-RW drive, a magnetic-optical device, a digital video recorder and a flash memory card [see Fig. 8, element 330]
- 5. **As per Claim 10**, Geiger as modified by Karpoff above teaches wherein said data transmission interface is one of an IDE interface, 1394 interface, a SCSI interface, a serial interface, a serial attached SCSI interface, a PCMCIA interface and a USB interface [see Fig. 11, element 264 Examiner takes official notice as it would be obvious to use one of the above interfaces in a computer system].

#### Conclusion

6. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

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the advisory action. In no event, however, will the statutory period for reply expire later than SIX

MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should

be directed to JASJIT S. VIDWAN whose telephone number is (571)272-7936. The examiner can

normally be reached on 8am - 5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tariq

Hafiz can be reached on 571.272.6729. The fax phone number for the organization where this

application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application

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1000.

/J. S. V./

Examiner, Art Unit 2182

7/18/08

/Tariq Hafiz/

Supervisory Patent Examiner, Art Unit 2182